

### SPECIFICATION AMENDMENTS

Please replace paragraph [0031] with the following:

Analog to digital converter 214 is coupled to digital physical layer receiver 216. Digital physical layer receiver 216 converts the digital sequence from analog to digital ~~receiver~~ converter 214 into a bit stream. Digital physical layer receiver ~~214~~ 216 will perform functions such as timing recovery, symbol recovery, equalization, despreading, error detection and correction, and bit slicing. Digital physical layer receiver is often implemented as digital hardware, a programmable digital signal processor, or both. Digital physical layer receiver 216 is coupled to MAC layer processor 218. MAC layer processor 218 controls access to the radio channel, including controlling channel selection.